Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	447	(multi-cycle or (two near2 more)) near5 path same ((input and output) or start$4 and end$4 near3 point)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 15:09
L2	1250641	(multi-cycle or (two near2 more)) near5 path (return or fedback or feedback)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 15:10
L3	447	1 and 2	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 15:03
L5	63	3 and ("716"/$.ccls. or "327"/$.ccls.)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 15:04
L6	34	(multi-cycle or (two near2 more) near5 cycle) near5 path same ((input and output) or start$4 and end$4 near3 point)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 15:11
L7	12	(multi-cycle or (two near2 more)) near5 path same (return or fedback or feedback) near5 loop	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 15:11
L8	0	6 and 7	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 15:11

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	709771	(connect$4 or interconnect$4 or path) same (((start$4 and end$4) near5 point) or ternimal or (input and output) or (fanin and fanout) or (fan-in and fan-out))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 09:58
L4	13906	(connect$4 or interconnect$4 or path) same (multi-cycl$3 or multiple near5 cycl$3 or multiple-cycl$4 or plurality near5 cycl$3 or (more near5 cycl$3))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 11:37
L5	152181	enable near5 (signal or path)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:02
L6	6405	3 and 4	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:02
L7	2063	5 and 6	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:02
L8	810650	(analy$7 or determin$4 or calculat$4) same (path or connect$4 or interconnect$4)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:49
L9	1567	7 and 8	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:04
L10	12384	(cell or element or device) same name same configur$6	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:05
L11	90	9 and 10	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:05
L12	4	11 and 716/1-19.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:49
L14	43	(analy$6 or determin$4) same (connect$4 or interconnect$4 or path) same (multi-cycl$3 or multiple near5 cycl$3 or multiple-cycl$4 or plurality near5 cycl$3 or (more near5 cycl$3)) same conver$6 same (((start$4 and end$4) near5 point) or ternimal or (input and output) or (fanin and fanout) or (fan-in and fan-out))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 11:11
L15	20936	(conversion or converting or generat$4) same clock same enable and memory	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:48

L16	4	14 and 15	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:46
L17	15469	3 and 15	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:48
L18	758	4 and 17	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:48
L19	624	8 and 18	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:49
L20	74	10 and 19	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:50
L21	4	20 and "716"/$.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 10:49
L22	1801	(analy$6 or determin$4) same (connect$4 or interconnect$4 or path) same (multi-cycl$3 or multiple near5 cycl$3 or multiple-cycl$4 or plurality near5 cycl$3 or (more near5 cycl$3))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 11:12
L23	2417	clock$4 same enable same memory same (ff or flipflop or flip-flop)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 11:13
L24	30	22 and 23	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 11:13
L25	22	24 and (converting or conversion or converted or convert)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 11:14
L26	29	(connect$4 or interconnect$4 or path) near5 (multi-cycl$3 or multiple near5 cycl$3 or multiple-cycl$4 or plurality near5 cycl$3 or (more near5 cycl$3)) and memory same (ff or flipflop or flip-flop) same enable	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 11:41
L27	35	(connect$4 or interconnect$4 or path) near5 (multi-cycl$3 or multiple near5 cycl$3 or multiple-cycl$4 or plurality near5 cycl$3 or (more near5 cycl$3)) and memory same (ff or flipflop or flip-flop) same enabl$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 11:41

L28	5	(connect$4 or interconnect$4 or path) near5 (multi-cycl$3 or multiple near5 cycl$3 or multiple-cycl$4 or plurality near5 cycl$3 or (more near5 cycl$3)) same memory same (ff or flipflop or flip-flop) same enabl$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 11:42
L29	3	(connect$4 or interconnect$4 or path) near5 (multi-cycl$3 or multiple near5 cycl$3 or multiple-cycl$4 or plurality near5 cycl$3) same memory same (ff or flipflop or flip-flop) same enabl$3	US-PGPUB; USPAT; EPO; JPO	OR	ON	2006/02/13 11:42